EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	16	(pipelin$4 or stripe) near2 FPGA and stage with (alternating or mix$4 or bypass$4)	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/28 14:42
L3	0	(pipelin$4 or stripe) near2 FPGA and stage with (alternating)	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/28 14:44
L4	0	(pipelin$4 or stripe) near2 FPGA with (alternating)	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/28 14:44
L5	6	stripe near2 FPGA	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/28 14:45
L6	8	stripe$1 near2 FPGA	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/28 14:46
L8	510	thread with (processor or circuit) configuration	US-PGPUB; USPAT; USOCR	WITH	ON	2009/11/28 16:55
S1	16083	transmi$6 with memor$3 with (rat$4 or speed or bandwidth)	US-PGPUB; USPAT	ADJ	ON	2008/05/07 16:36
S2	21	transmi$6 with memor$3 with (high$3 or low$3) (rat$4 or speed or bandwidth) with (internal or state) near3 (circuit or logic or machine)	US-PGPUB; USPAT	ADJ	ON	2008/05/07 16:37
S3	5	(second$3 or another or other) (path or connect$4 or bus or transmi$6) with memor$3 with (high$3 or low$3) (rat$4 or speed or bandwidth) with (internal or state) near3 (circuit or logic or machine)	US-PGPUB; USPAT	ADJ	ON	2008/05/07 16:43
S4	1	("6034538").PN.	US-PGPUB; USPAT	OR	OFF	2008/05/08 12:16
S5	61930	(reconfigur$4 or (re configur$4) or configur$4) near5 (successive or after or next or follow$4)	US-PGPUB; USPAT	ADJ	ON	2008/05/08 17:56

S6	1	("20070038971").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/11/18 13:40
S7	947	716/16.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/11/18 13:40
S8	458	712/11.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/11/18 13:41
S9	27	(S7 or S8) and reconfigur$5 with row	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/11/18 13:51
S10	1	("20030200237").PN.	US-PGPUB; USPAT	OR	OFF	2009/05/06 17:53
S11	163	(reconfigur$4 or (re configur$4) or configur$4) near5 (successive or after or next or follow$4) with process$4 with stage	US-PGPUB; USPAT	ADJ	ON	2009/05/06 23:12
S12	0	("2003/0200237").URPN.	USPAT	ADJ	ON	2009/05/06 23:18
S13	45	("4541071" | "4942319" | "5493239" | "5508636" | "5572148" | "5640106" | "5654650" | "5760607" | "5773993" | "5838165"). PN. OR ("6034538"). URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/05/06 23:18
S14	923	(reconfigur$4 or (re configur$4) or configur$4) with process flow	US-PGPUB; USPAT	ADJ	ON	2009/05/06 23:35
S15	100	demodulat$4 with loop with determin$4 with filter	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:08
S16	0	demodulat$4 with loop with determin$4 with filter with (reconfigur$6)	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:08
S17	1	demodulat$4 with loop filter with (positive or negative) near3 determin$4	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:10
S18	1	demodulat$4 with (loop filter and (positive or negative) near3 determin$4)	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:10
S19	12	demodulat$4 with (loop filter) and ((positive or negative) near3 determin$4)	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:11

S20	594	positive negative determin $6	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:12
S21	5	positive negative determin $6 with demodulat$4	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:12
S24	1	positive negative determin $6 with demodulat$4 and loop and filter	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:23
S25	14	(demodulat$4 with loop with filter) and ((positive or negative) near3 determin$4) and (reconfigur$6 or configur $6 or program$6) with process$4	US-PGPUB; USPAT	ADJ	ON	2009/05/07 17:48
S26	5005	demodulat$4 with (reconfigur$6 or configur $6 or program$6) with process$4	US-PGPUB; USPAT	ADJ	ON	2009/05/07 18:15
S27	1310	demodulat$4 with (reconfigur$6 or configur $6) near4 process$4	US-PGPUB; USPAT	ADJ	ON	2009/05/07 18:15
S28	239	demodulat$4 with (reconfigur$6 or configur $6) near4 process$4 and FPGA	US-PGPUB; USPAT	ADJ	ON	2009/05/07 18:15
S29	12	demodulat$4 with (loop filter) and ((positive or negative) near3 determin $4)	US-PGPUB; USPAT	ADJ	ON	2009/05/07 22:22
S30	8	S29 and processor	US-PGPUB; USPAT	ADJ	ON	2009/05/07 22:22
S31	449	712/13.ccls. or 712/15. ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 17:16
S32	193	(pipelin$4 or stripe) near2 FPGA	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 17:40
S33	8	(pipelin$4 or stripe) near2 FPGA with (alternat$4 or multiple)	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 17:45
S34	1	("6034538").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2009/11/21 17:51
S35	2	(("5933642") or ("5778439")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2009/11/21 17:55

S36	0	(pipelin$4 or stripe) near2 FPGA with (alternat$4 or mix$4 or bypass$4)	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 18:13
S37	27	(pipelin$4 or stripe) near2 FPGA and stage with (alternat$4 or mix$4 or bypass$4)	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 18:14
S38	104	temporal$3 partition$4	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 18:18
S39	193	(pipelin$4 or stripe) near2 FPGA	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 18:18
S40	8	S38 and S39	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 18:18
S41	10	(S38 or S39) with parallel	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 18:28
S42	12	(S38 or S39) with (parallel or concurrent$2 or simultaneous$3)	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/21 18:40
S43	0	switch$4 threads every cycle	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/11/27 14:49
S44	40	switch$4 threads every cycle	US-PGPUB; USPAT; USOCR	WITH	ON	2009/11/27 14:49
S46	1	("20020002667").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2009/11/27 16:55
S47	10	("2002/0002667").URPN.	USPAT	ADJ	ON	2009/11/27 16:56

11/28/2009 5:35:56 PM
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